

A

B

C

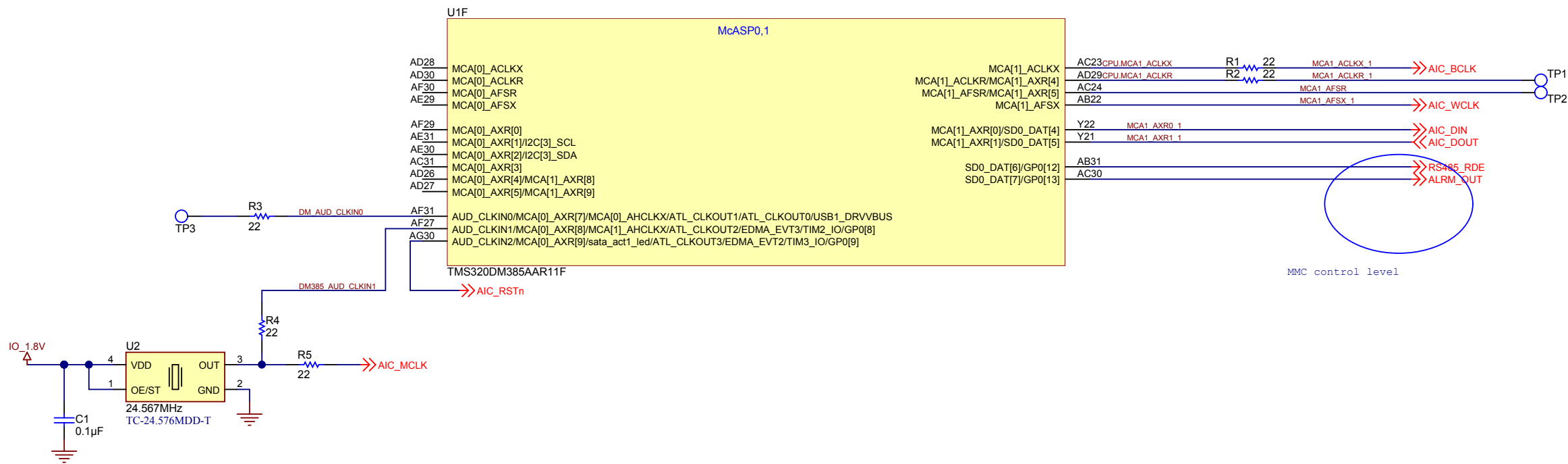
D

A

B

C

D



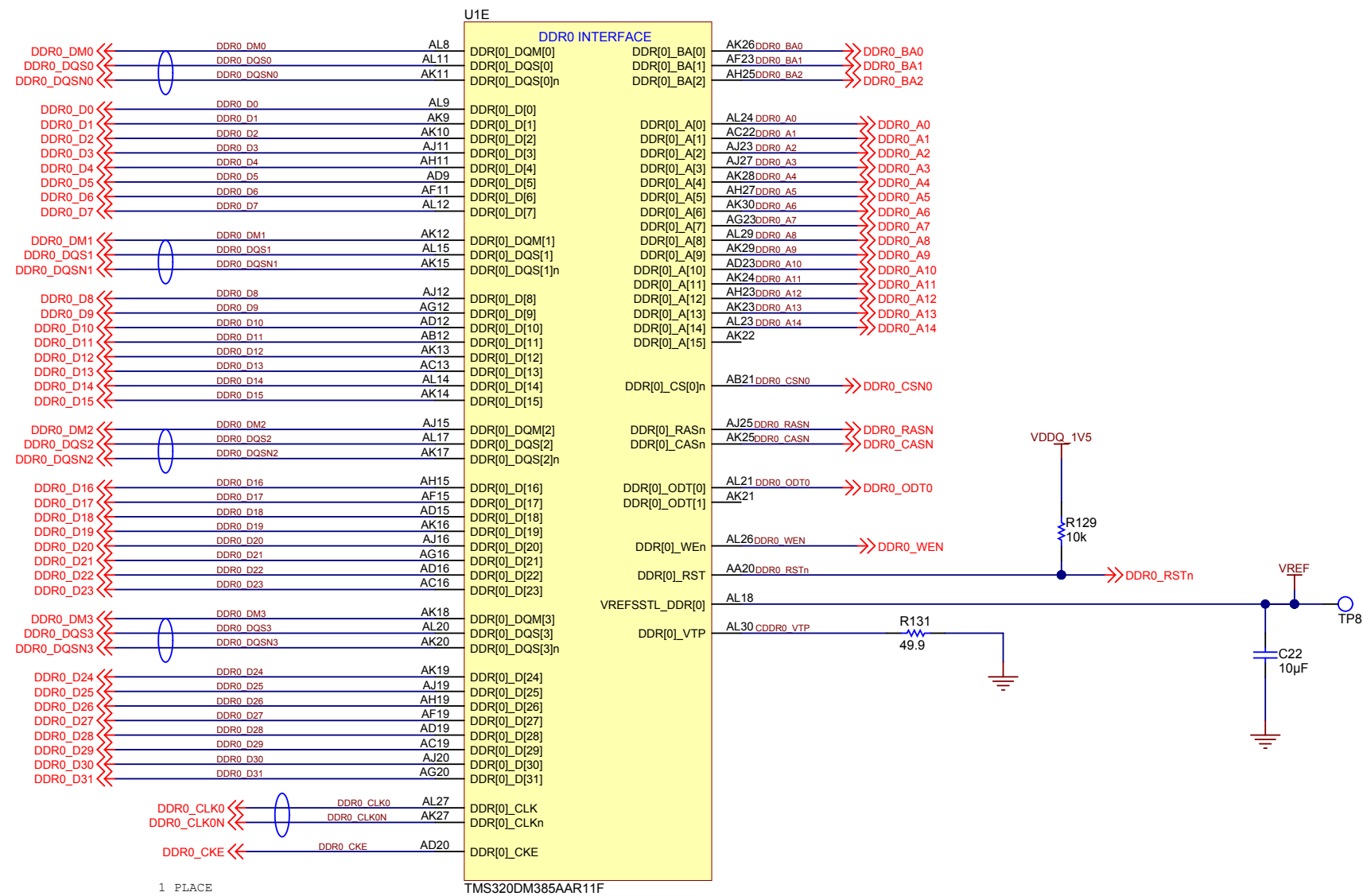
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Designed for: Public Release		Mod. Date: 11/12/2013	
Project Title: DM385 IP-CAMERA			
Number: SAT0008		Rev: E6	
SVN Rev: Not in version control		Assembly Variant: 001	
Drawn By:		File: 002_DM385_McASP_SchDoc	
Engineer: Scott McElroy		Contact: http://www.ti.com/support	
		Sheet: 2 of 18	
		Size: B	
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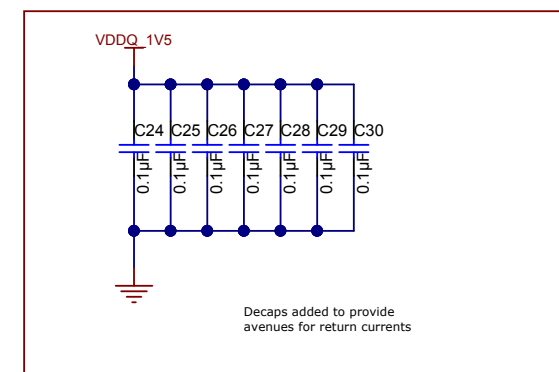
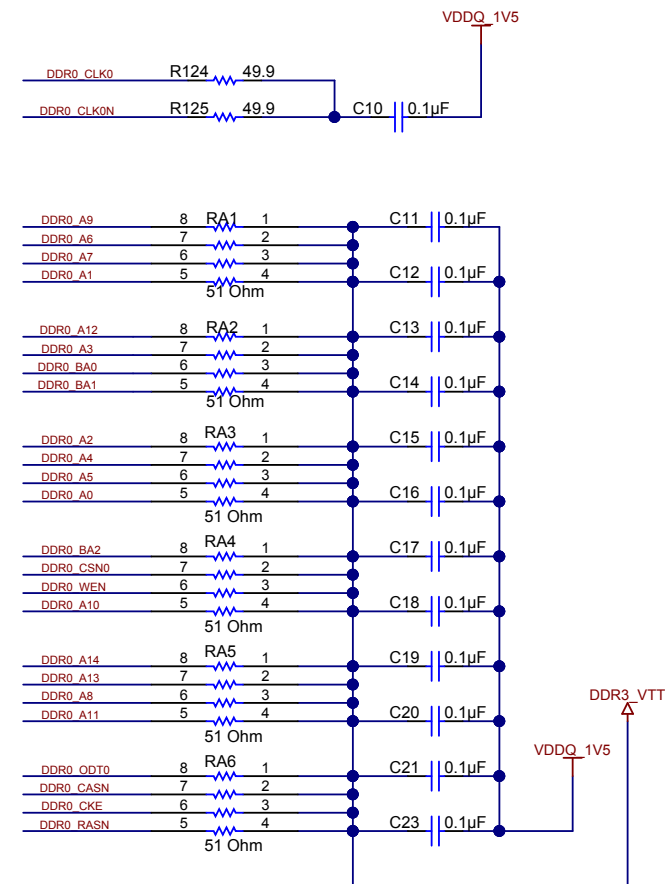




4 PLACES
DIFFERENTIAL PAIR
100 OHM DIFFERENTIAL
IMPEDANCE
SHORT AND STRAIGHT AS
POSSIBLE,
MINIMUM NUMBER OF VIAS



DDR0 TERMINATIONS



A

A

B

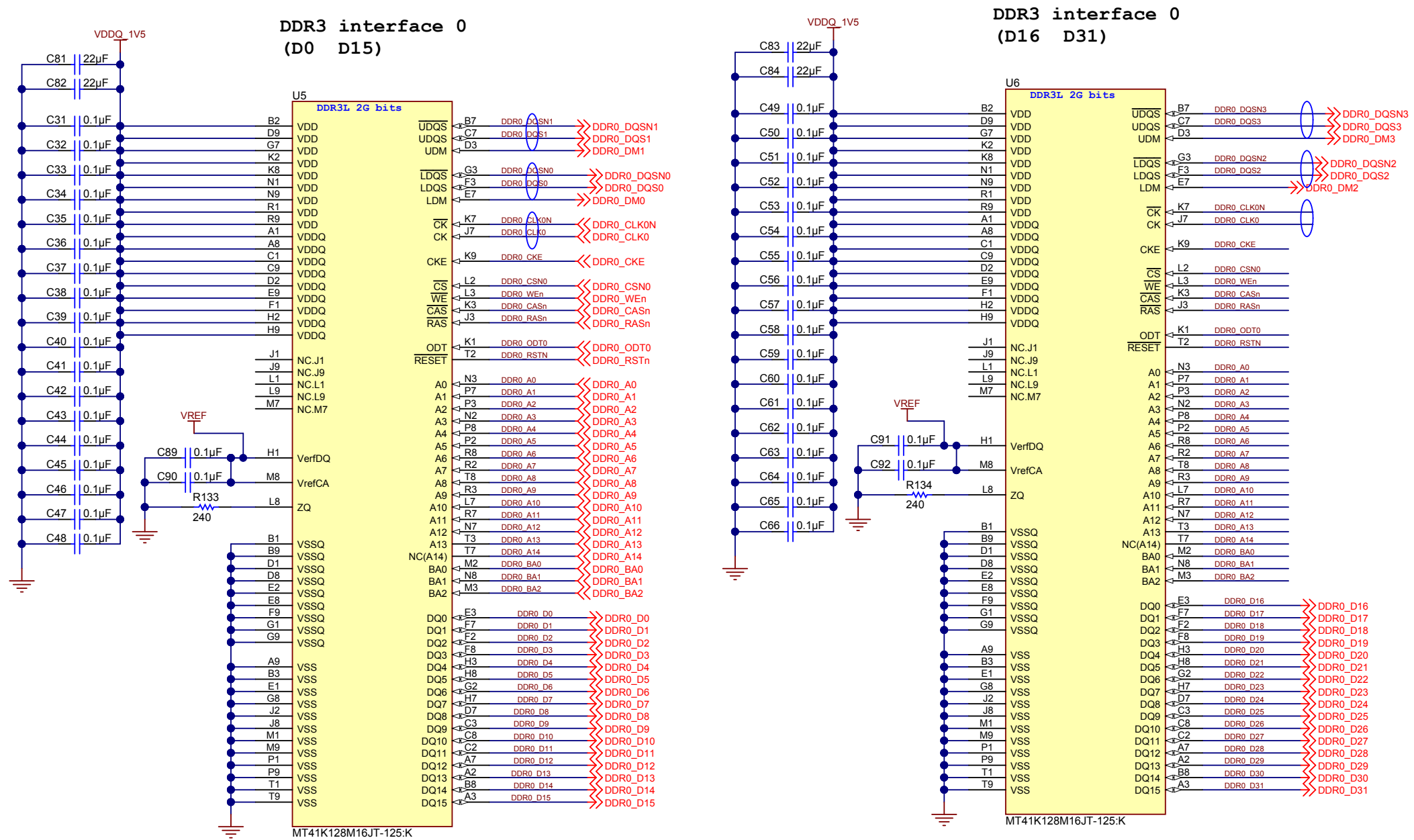
B

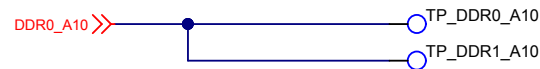
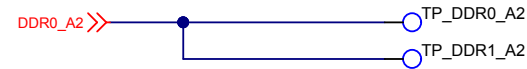
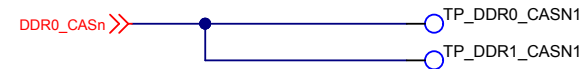
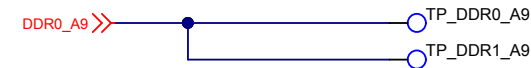
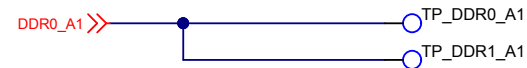
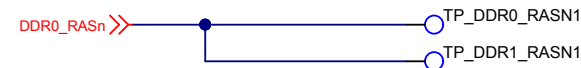
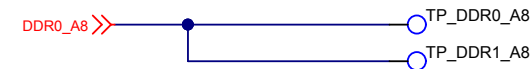
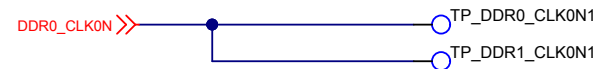
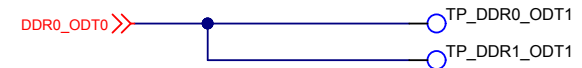
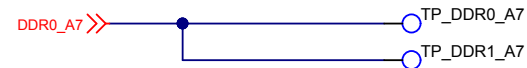
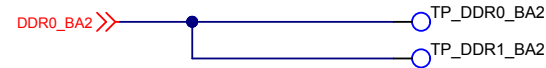
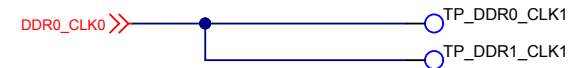
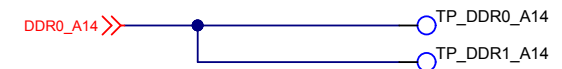
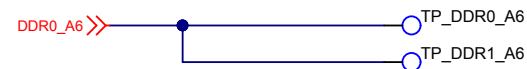
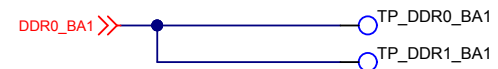
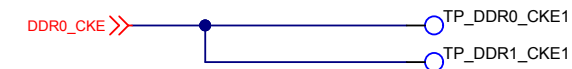
C

C

D

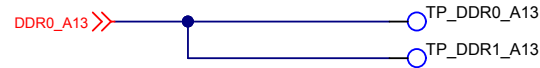
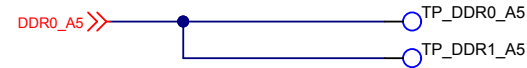
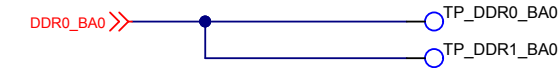
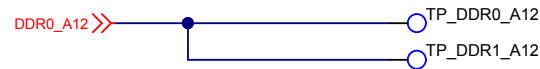
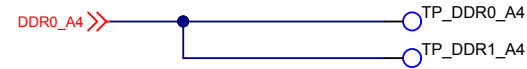
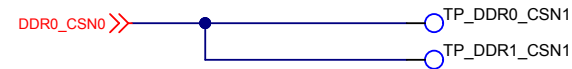
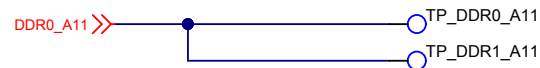
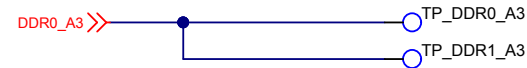
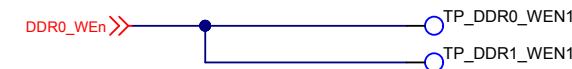
D



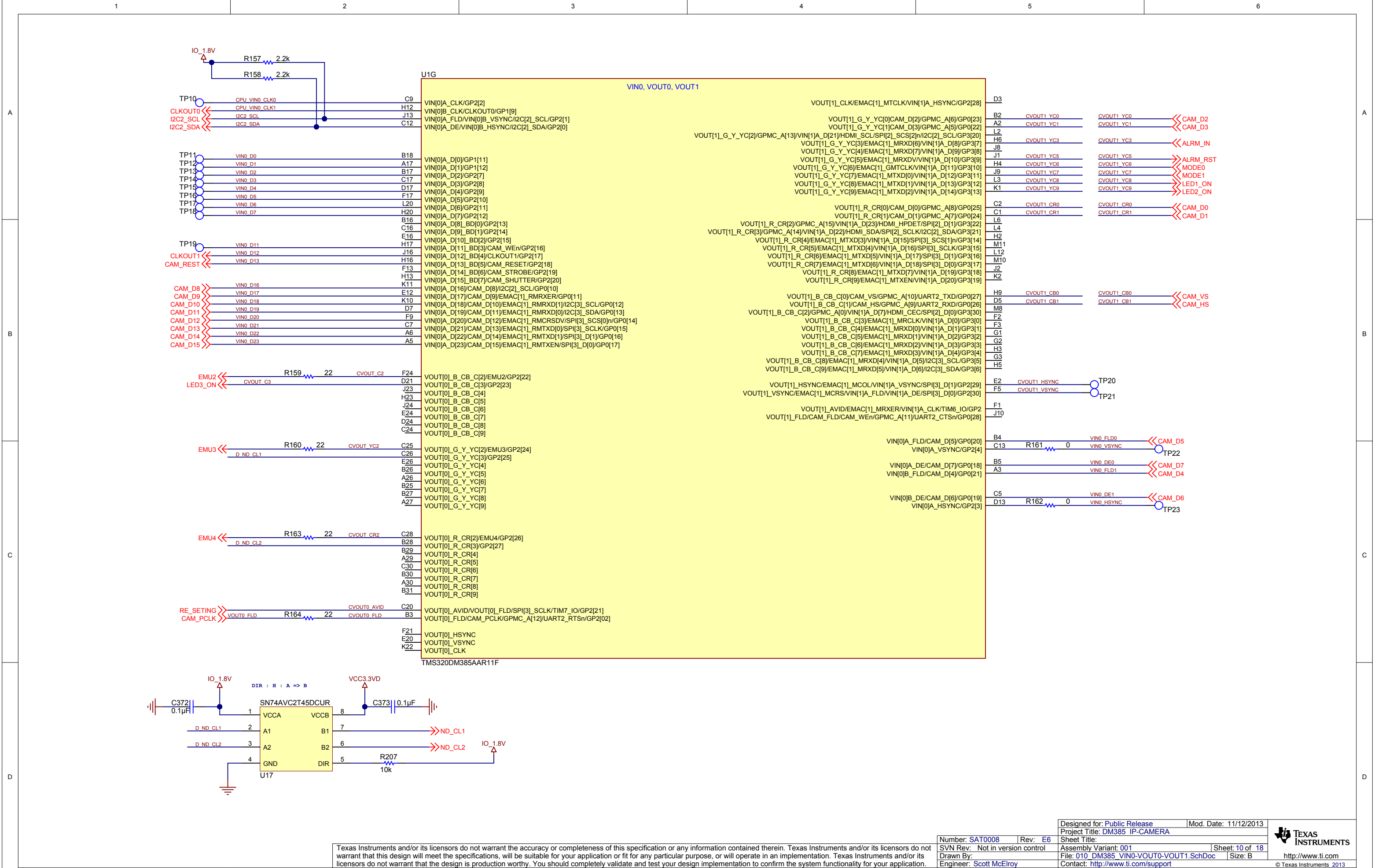


NOTE:

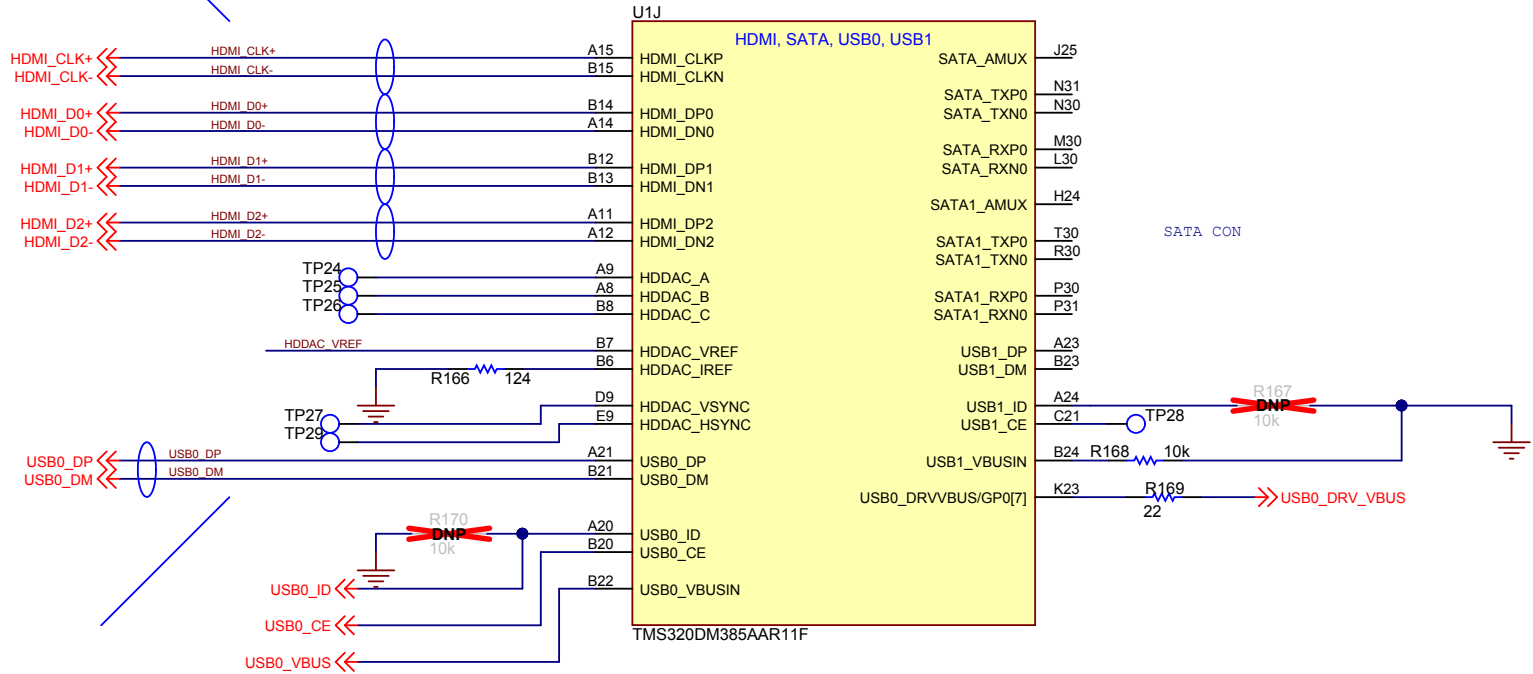
- 1) Through Hole Test points are used to check compliance with the routing guidelines. This helps to generate the Net length Report.
- 2) Designers need to replace Test point with a Via of the same size.



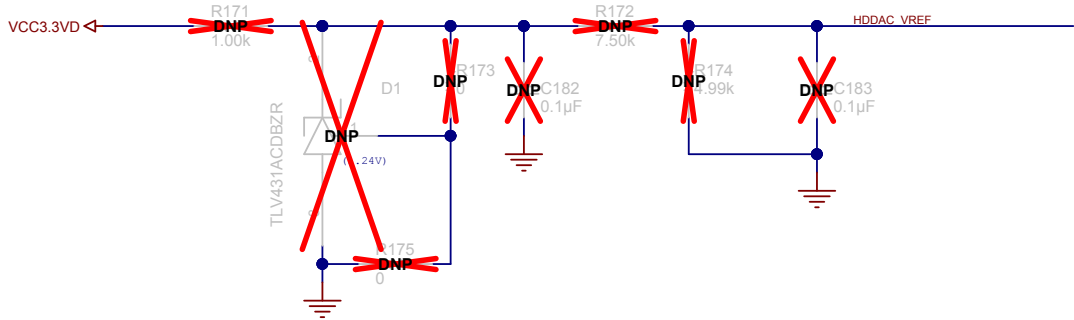




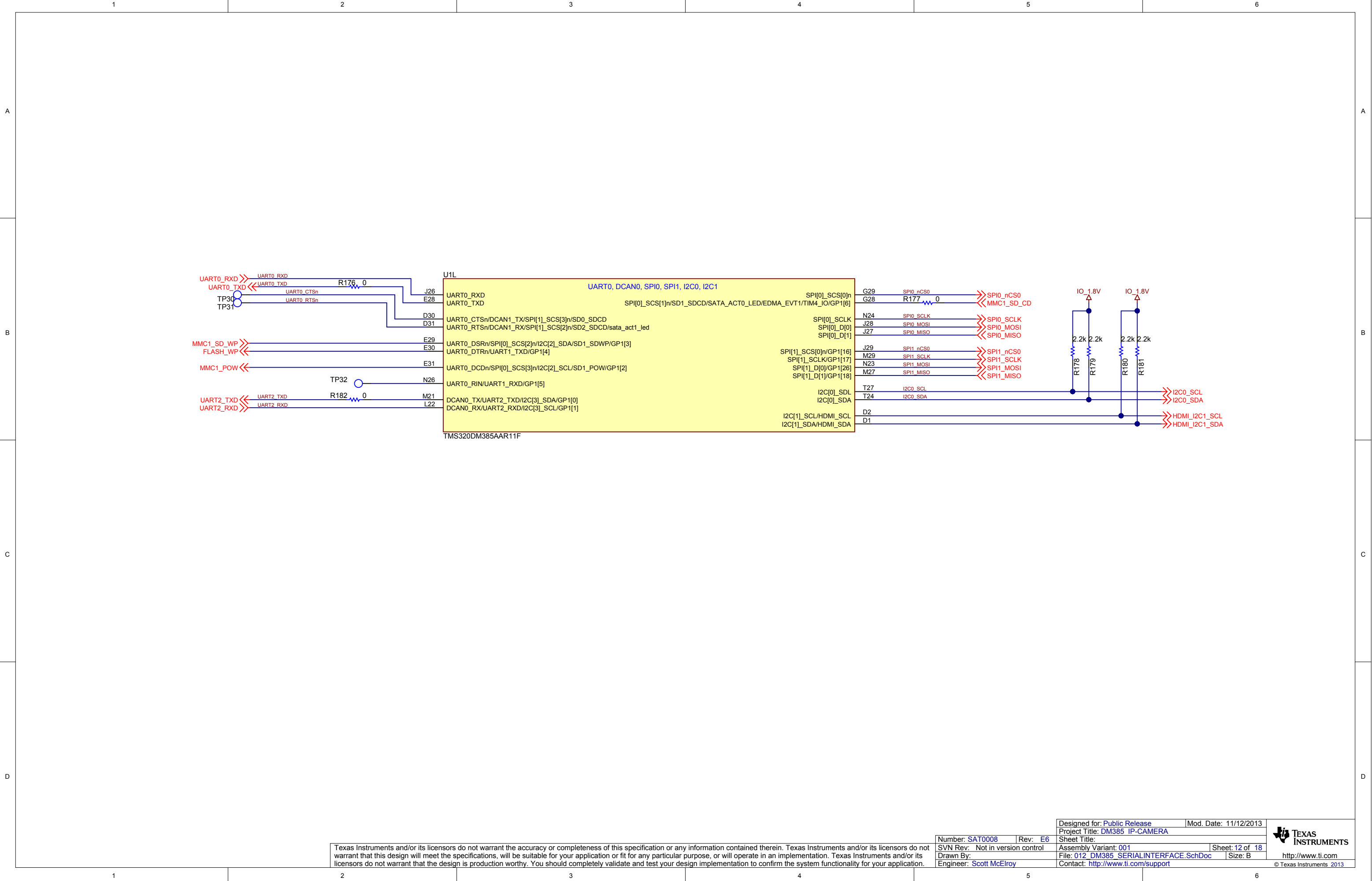
4 PLACES
DIFFERENTIAL PAIR
100 OHM DIFFERENTIAL
IMPEDANCE
SHORT AND STRAIGHT AS
POSSIBLE,
MINIMUM NUMBER OF VIAS



HD_DAC NOT USED IN THIS DESIGN

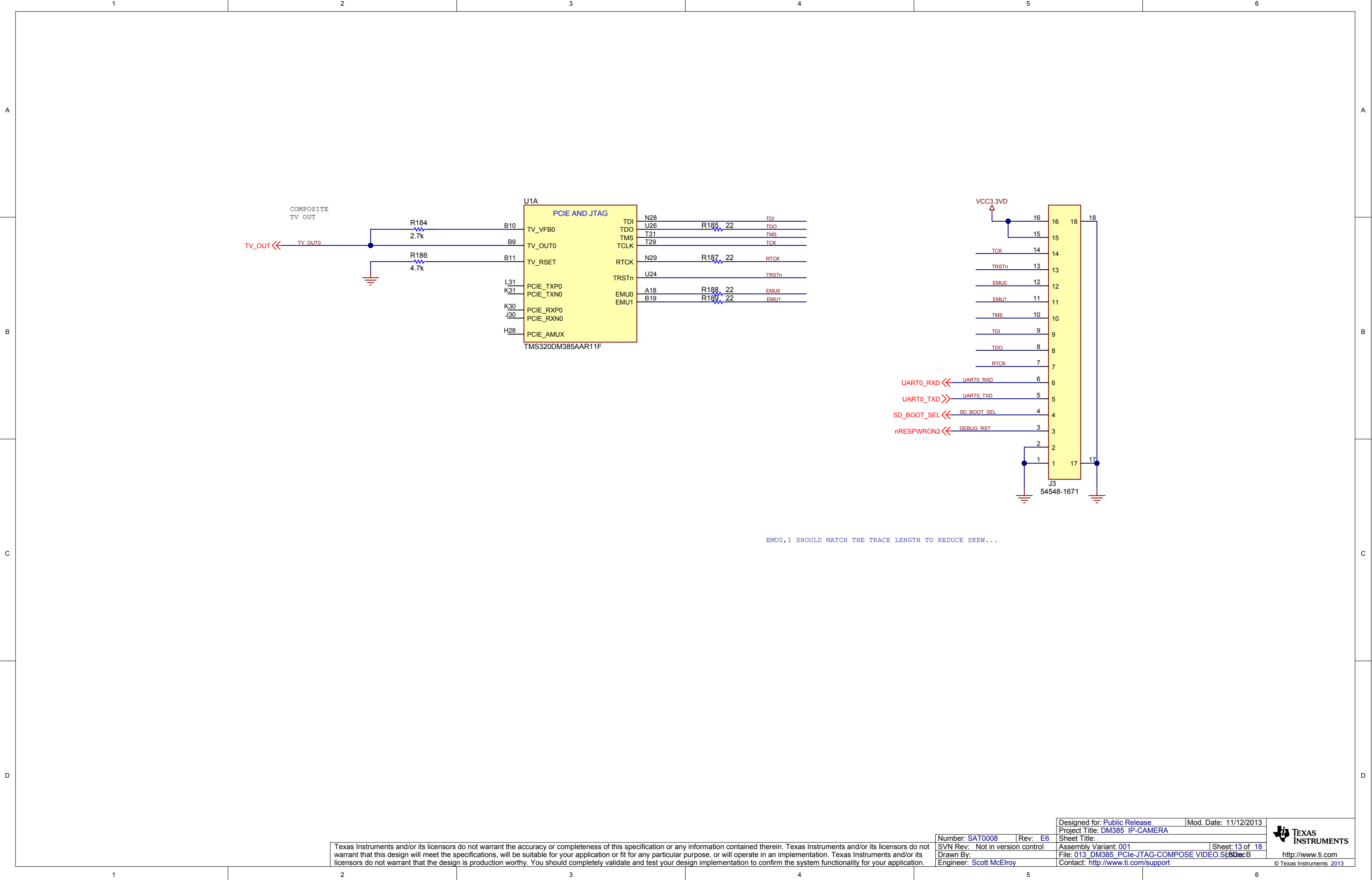


DIFFERENTIAL PAIR
90 OHM DIFFERENTIAL
IMPEDANCE
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MINIMUM NUMBER OF VIAS

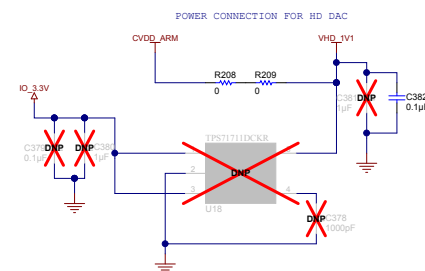
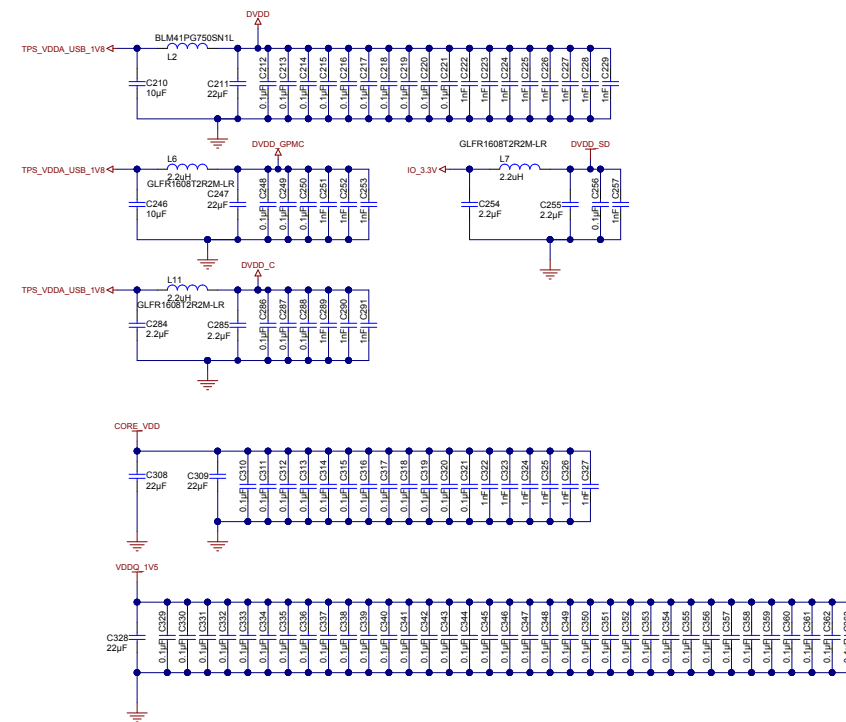
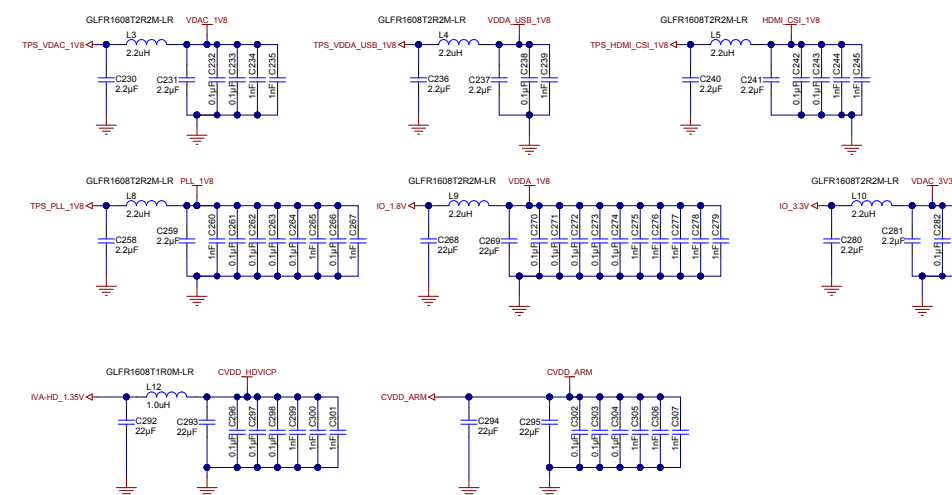
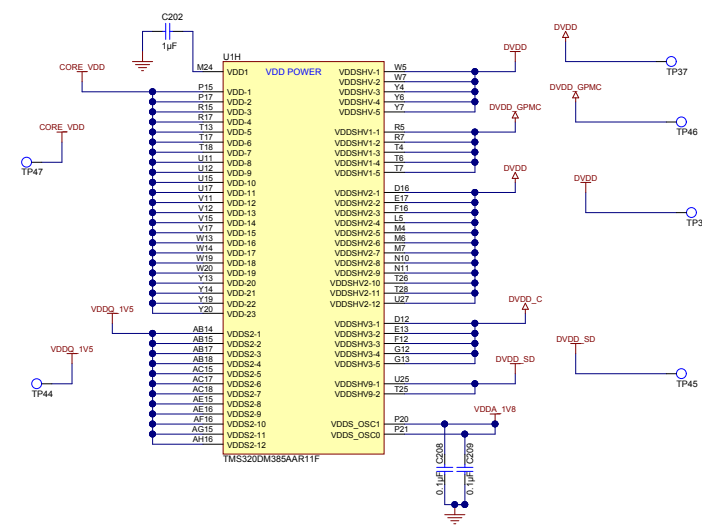
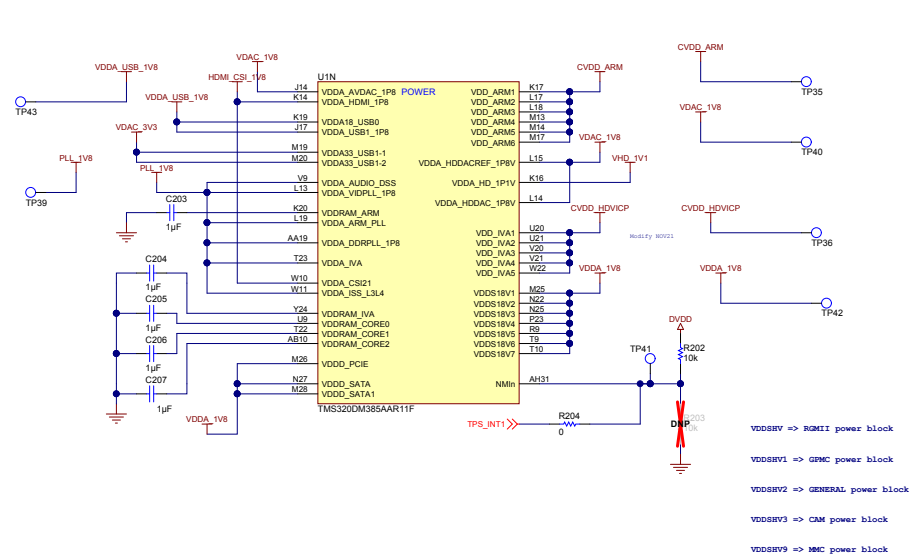


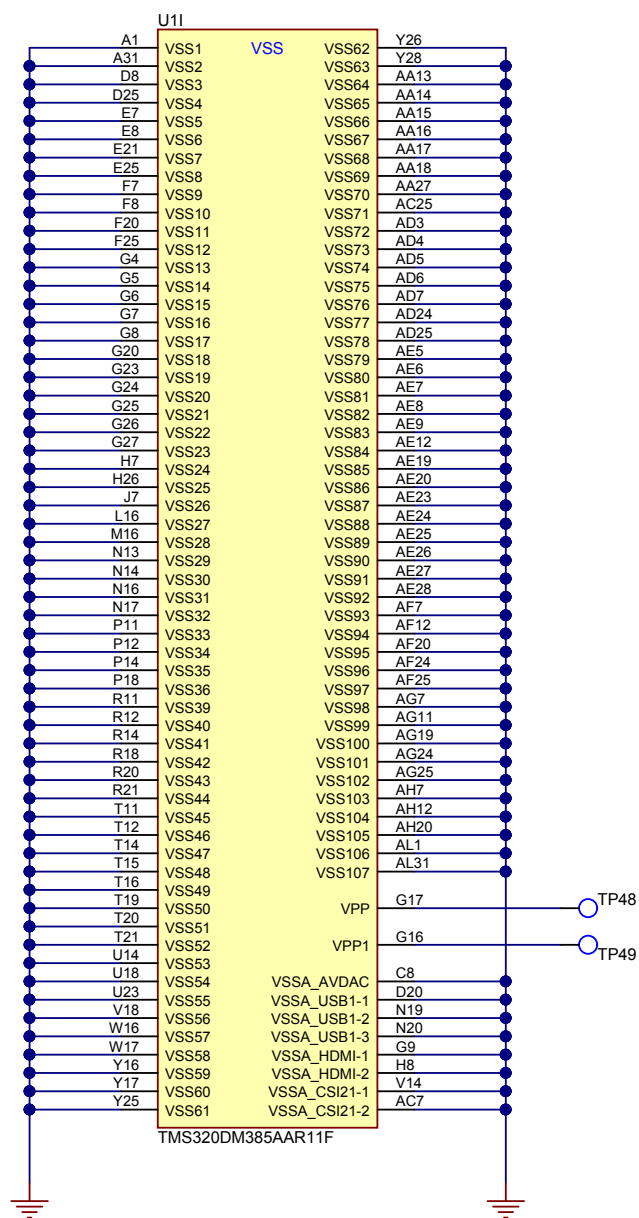
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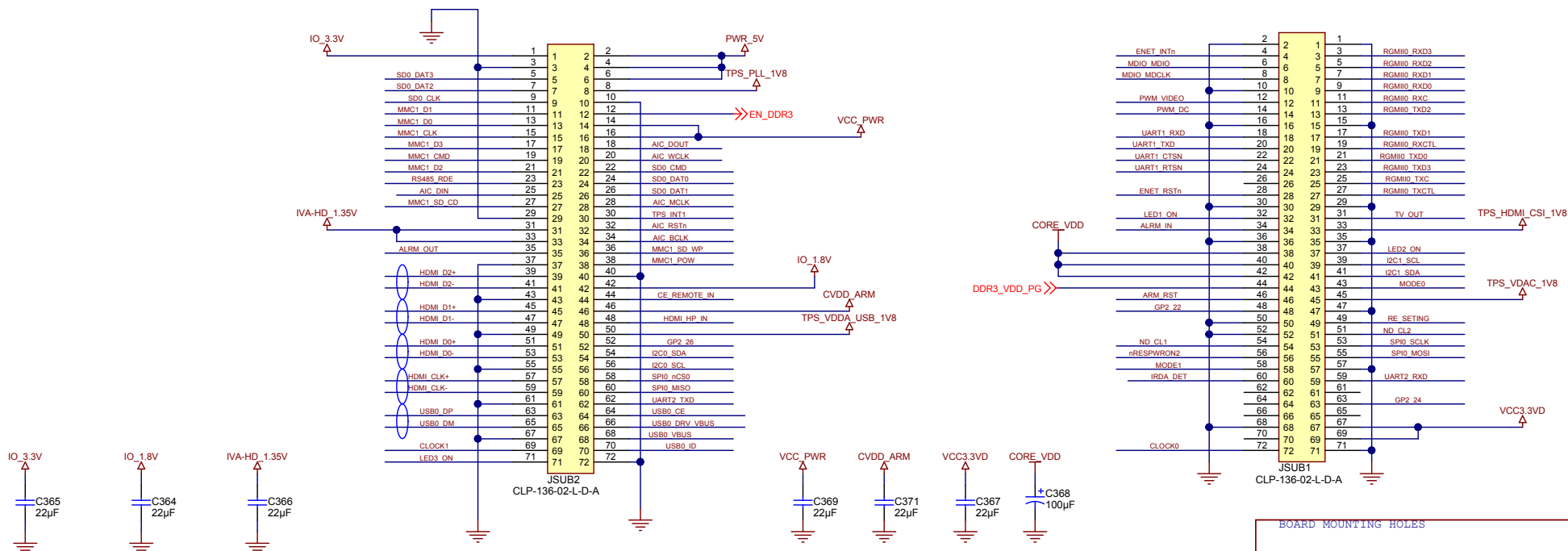












UART1_RXD >> UART1_RXD
UART1_TXD >> UART1_TXD
UART1_CTSN >> UART1_CTSN
UART1_RTSN >> UART1_RTSN

MDIO_MDCLK >> MDIO_MDCLK
MDIO_MDIO >> MDIO_MDIO

ENET_RSTn >> ENET_RSTn
ENET_INTn >> ENET_INTn

nRESPWRON2 >> nRESPWRON2

USB0_DP >> USB0_DP
USB0_DM >> USB0_DM

USB0_ID >> USB0_ID
USB0_CE >> USB0_CE
USB0_VBUS >> USB0_VBUS
USB0_DRV_VBUS >> USB0_DRV_VBUS

4 places
differential pairs 100 ohm

RGMII0_RXC >> RGMII0_RXC
RGMII0_RXCTL >> RGMII0_RXCTL
RGMII0_RXD0 >> RGMII0_RXD0
RGMII0_RXD1 >> RGMII0_RXD1
RGMII0_RXD2 >> RGMII0_RXD2
RGMII0_RXD3 >> RGMII0_RXD3

RGMII0_TXC >> RGMII0_TXC
RGMII0_TXCTL >> RGMII0_TXCTL
RGMII0_TXD0 >> RGMII0_TXD0
RGMII0_TXD1 >> RGMII0_TXD1
RGMII0_TXD2 >> RGMII0_TXD2
RGMII0_TXD3 >> RGMII0_TXD3

1 places
differential pairs 90 ohm

HDMI_CLK+ >> HDMI_CLK+
HDMI_CLK- >> HDMI_CLK-
HDMI_D0+ >> HDMI_D0+
HDMI_D0- >> HDMI_D0-
HDMI_D1+ >> HDMI_D1+
HDMI_D1- >> HDMI_D1-
HDMI_D2+ >> HDMI_D2+
HDMI_D2- >> HDMI_D2-
I2C1_SCL >> HDMI_I2C1_SCL
I2C1_SDA >> HDMI_I2C1_SDA
CE_REMOTE_IN >> CE_REMOTE_IN
HDMI_HP_IN >> HDMI_HP_IN

EMU2 << GP2_22 TP50

EMU3 << GP2_24 TP51

EMU4 << GP2_26 TP52

TPS_INT1 >> TPS_INT1

nRESPWRON2 << nRESPWRON2

CLOCK1 >> R205 CLKOUT1

CLOCK0 >> DNP CLKOUT0

UART2_RXD >> UART2_RXD
UART2_TXD >> UART2_TXD

TV_OUT >> TV_OUT
PWM_DC >> PWM_DC
PWM_VIDEO >> PWM_VIDEO

MMC1_POW >> MMC1_POW
MMC1_SD_WP >> MMC1_SD_WP
MMC1_SD_CD >> MMC1_SD_CD
MMC1_CLK >> MMC1_CLK
MMC1_CMD >> MMC1_CMD
MMC1_D0 >> MMC1_D0
MMC1_D1 >> MMC1_D1
MMC1_D2 >> MMC1_D2
MMC1_D3 >> MMC1_D3

SD0_CLK >> SD0_CLK
SD0_CMD >> SD0_CMD
SD0_DAT0 >> SD0_DAT0
SD0_DAT1 >> SD0_DAT1
SD0_DAT2 >> SD0_DAT2
SD0_DAT3 >> SD0_DAT3

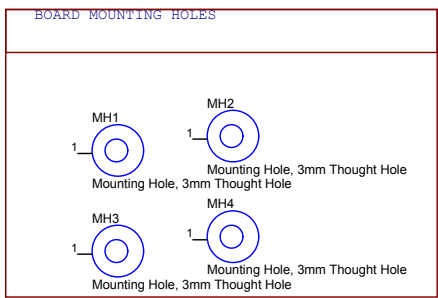
TPS_INT1 >> TPS_INT1

AIC_DOUT >> AIC_DOUT
AIC_DIN >> AIC_DIN
AIC_MCLK >> AIC_MCLK
AIC_BCLK >> AIC_BCLK
AIC_WCLK >> AIC_WCLK
AIC_RSTn >> AIC_RSTn

SPI0_MOSI >> SPI0_MOSI
SPI0_SCLK >> SPI0_SCLK
SPI0_nCS0 >> SPI0_nCS0
SPI0_MISO >> SPI0_MISO

RS485_RDE >> RS485_RDE
I2C0_SCL >> I2C0_SCL
I2C0_SDA >> I2C0_SDA

IRDA_DET >> IRDA_DET
ND_CL2 >> ND_CL2
ND_CL1 >> ND_CL1
RE_SETTING >> RE_SETTING
LED3_ON >> LED3_ON
ALARM_IN >> ALARM_IN
ALARM_OUT >> ALARM_OUT
ARM_RST >> ARM_RST
MODE0 >> MODE0
MODE1 >> MODE1
LED1_ON >> LED1_ON
LED2_ON >> LED2_ON



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Number: SAT0008 Rev: E6
SVN Rev: Not in version control
Drawn By:
Engineer: Scott McElroy

Designed for: Public Release
Project Title: DM385 IP-CAMERA
Sheet Title:
Assembly Variant: 001
File: 017_BOARD TO BOARD INTERFACE.SchDoc
Contact: http://www.ti.com/support

Mod. Date: 11/12/2013
Sheet 17 of 18
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A

A



FID1

B

B



FID2



FID3



FID4



FID5



FID6

PCB Number: SAT0008
PCB Rev: E6

PCB
LOGO
Texas Instruments

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

C

C

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4


Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2., unless otherwise specified.

D

D

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Number: SAT0008		Rev: E6		http://www.ti.com
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Drawn By:		File: Hardware ANSI-B.SchDoc		© Texas Instruments 2013
Engineer: Scott McElroy		Size: B		
		Contact: http://www.ti.com/support		

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